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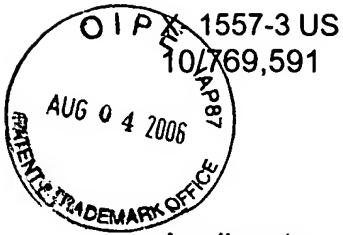
TRANSMITTAL FORM <small>(to be used for all correspondence after initial filing)</small> Mail Stop: AMENDMENT Express Mail Receipt No. Total Number of Pages in This Submission	Application / Conf. No.	10/769,591 / 8956
	Filing Date	January 30, 2004
	First Named Inventor	Chidambr R. Kulkarni
	Examiner Name	Tuyen P. To
	Art Unit	2825
	Patent No.	
	Attorney Docket Number	X-1557-3 US

ENCLOSURES (check all that apply)		
<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers (with Recordation Cover Sheet)	<input type="checkbox"/> After Allowance Communication to
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Date	August 1, 2006	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040

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PATENT
Conf. No.:8956

IN THE UNITED STATES PATENT OFFICE

Applicants: Chidamber R. Kulkarni et al.
Assignee: Xilinx, Inc.
Title: Method and Apparatus for Application-Specific Programmable
Memory Architecture and Interconnection Network on a Chip
Serial No.: 10/769,591 File Date: 1/30/2004
Examiner: Tuyen P.To Art Unit: 2825
Docket No.: X-1557-3 US Conf. No.: 8956

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AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

Dear Sir:

In response to the Office Action mailed from the Patent Office on May 5, 2006,
please replace/substitute the following claims as indicated.

Amendments to the specification begin on page 2 of this paper.

Amendments to the claims are reflected in the list of claims that begins on
page 3 of this paper.

Remarks begin on page 7 of this paper.